

What is claimed is:SUB ~~1~~

1 A decoding method of moving image signal, being a decoding
2 method of moving image signal for decoding at least two or more motion vectors
3 relating to the present processing pixel block, compensating the motion of the
4 decoded frame corresponding to each one of said two or more motion vectors, and
5 generating two or more predicted images relating to the present processing pixel
6 block,

7 wherein the predicted image used in reconstruction of the present
8 processing pixel block is selected depending on presence or absence of decoding
9 error contained in said two or more predicted images.

1 2. A decoding method of moving image signal of claim 1, wherein
2 if there are plural predicted images free from decoding error in said two or more
3 predicted images,

4 the predicted image produced from the latest decoded frame in time
5 out of said plural predicted images free from decoding error is used in
6 reconstruction of the present processing pixel block.

1 3. A coding method of moving image signal, being a coding method
2 of moving image signal for detecting and coding at least two or more motion
3 vectors relating to the present processing pixel block, characterized by:

4 inter-coding the present processing pixel block when the correlation
5 of two or more predicted images compensated of motion by said two or more
6 motion vectors is high, and

7 intra-coding the present processing pixel block when the correlation
8 of said two or more predicted images is low.

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4 decoding means is contradictory to a specified standard.

1 7. A decoding apparatus of moving image signal of claim 5,
2 wherein the memory means stores bit errors in plural frames by plotting the pixel
3 blocks in which bit error is detected in each frame in a map form.

1 8. A decoding apparatus of moving image signal of claim 7,
2 wherein the memory means comprises plural decoding error map memories
3 storing each frame consecutive in time, and also has changeover means, and
4 therefore said plural decoding error map memories are changed over by said
5 changeover means, and issued.

1 9. A coding apparatus of moving image signal comprising:
2 motion vector detecting means for detecting at least two or more
3 motion vectors relating to the present processing pixel block,
4 motion compensation means for issuing plural predicted images from
5 the output of said motion vector detecting means, and
6 intra/inter judging means for inter-coding the present processing
7 pixel block when the correlation of two or more predicted images compensated of
8 motion by said two or more motion vectors as the output of said motion
9 compensation means is high, and intra-coding the present processing pixel block
10 when the correlation of said two or more predicted images is low.

1 10. A coding apparatus of moving image signal of claim 9, further
2 comprising:

3 predicted image combining means for combining two or more
4 predicted images compensated by said two or more motion vectors, and

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